

In re Patent Application of:
SONZOGNI ET AL.
Serial No. **Not Yet Assigned**
Filed: **Herewith**

checking, as a function of the at least one check bit, whether access to locations in the memory is authorized for the new entity.

21. A method according to Claim 20, further comprising storing a second code in a second register of the microprocessor for an application program active when a last call instruction was sent.

22. A method according to Claim 21, wherein the second register can not be directly accessed.

23. A method according to Claim 20, wherein each entity is one of the plurality of application programs.

24. A method according to Claim 20, wherein each entity causes a hardware event.

25. A method according to Claim 24, wherein the hardware event resets the microprocessor.

26. A method according to Claim 20, wherein the first register is updated in response to the return instruction.

27. A method according to Claim 20, wherein checking comprises providing a control signal to the microprocessor for providing access to the locations of the memory if the new entity is authorized.

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28. A method according to Claim 20, wherein checking comprises comparing the address locations to be accessed in the memory and the first code in the first register.

29. A method for securing access to a chip card comprising a microprocessor and a memory connected thereto for storing a plurality of application programs, the method comprising:

storing a first code, on at least one check bit, in a first register of the microprocessor for an application program to be executed;

updating the first register based upon a call instruction and a return instruction during execution of a new application program; and

checking, as a function of the at least one check bit, whether access to locations in the memory is authorized for the new application program.

30. A method according to Claim 29, further comprising storing a second code in a second register of the microprocessor for an application program active when a last call instruction was sent.

31. A method according to Claim 29, wherein the second register can not be directly accessed.

32. A method according to Claim 29, wherein each application program causes a hardware event.

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33. A method according to Claim 33, wherein the hardware event resets the microprocessor.

34. A method according to Claim 29, wherein the first register is updated in response to the return instruction.

35. A method according to Claim 29, wherein checking comprises providing a control signal to the microprocessor for providing access to the locations of the memory if the new application program is authorized.

36. A method according to Claim 29, wherein checking comprises comparing the address locations to be accessed in the memory and the first code in the first register.

REMARKS

It is believed that all of the claims are patentable over the prior art. For better readability and the Examiner's convenience, the newly submitted claims differ from the translated counterpart claims which are being canceled. The newly submitted claims do not represent changes or amendments that narrow the claim scope for any reason related to the statutory requirements for patentability. Accordingly, after the Examiner completes a thorough examination and finds the claims patentable, a Notice of Allowance is respectfully requested in due course. Should the Examiner determine any minor informalities that need to be addressed, he is encouraged to contact the undersigned attorney at the telephone number below.